



AJEENKYA

D Y PATIL UNIVERSITY

End Term Examinations (December 2018)

School: School of Information Technology

Course: Computer Architecture & Organization

Semester: III

Max Marks: 30

Program: BTech (MACT & CTIS)

Course Code: CSC221

Duration (mins): 60

Note- 1. Figures to the right indicates full marks.

2. Attempt any three questions.

Q1)

- a) Explain the implementation of BUS using Multiplexer or Three state buffer. (5)
- b) Explain Programmed I/O Mode of Data transfer. (5)

Q2)

- a) Explain Control Unit. (2)
- b) Explain Instruction Cycle in detail. (8)

Q3)

- a) List any 4 Micro operations. (2)
- b) Explain Direct Memory Access Controller. (2)
- c) Explain different addressing modes. (6)

Q4)

- a) Explain I/O Interface. (5)
- b) Explain Shift Operations with example. (5)

Q5)

- a) Explain Instruction cycle and phases in it. (2)
- b) Explain Booths Algorithm with the help of Flowchart and Multiply $6 * -4$. (8)